[FIG.1]

TRANSMIT SIGNAL

- 101 CONTROL SECTION
- 102 SPREADING SECTION
- 5 103 IFFT SECTION
 - 104 GI INSERTION SECTION
 - 105 GI INSERTION SECTION
 - 106 GI INSERTION SECTION
 - 107 SELECTION SECTION
- 10 RETRANSMISSION INFORMATION

[FIG.2]

FREQUENCY

- #4m m'th chip of signals 3k+1 through 4k of time T

 m'th chip of signals 3k+1 through 4k of time 2T

 m'th chip of signals 3k+1 through 4k of time 3T

m'th chip of signals k+1 through 2k of time 2T m'th chip of signals k+1 through 2k of time 3T

#m+1 1st chip of signals k+1 through 2k of time T

1st chip of signals k+1 through 2k of time 2T

1st chip of signals k+1 through 2k of time 3T

#m m'th chip of signals 1 through k of time T

m'th chip of signals 1 through k of time 2T

m'th chip of signals 1 through k of time 3T

10

#1 1st chip of signals 1 through k of time T
 1st chip of signals 1 through k of time 2T
 1st chip of signals 1 through k of time 3T
TIME

15

[FIG.3]

START

ST301 RETRANSMISSION?

ST302 RETRANSMISSION COUNT = 1?

20 ST303 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/8
OF EFFECTIVE SYMBOL LENGTH

ST304 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/4
OF EFFECTIVE SYMBOL LENGTH

ST305 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 3/8

25 OF EFFECTIVE SYMBOL LENGTH

ST306 OUTPUT

END

[FIG.4]

EFFECTIVE SYMBOLS

[FIG.5]

5 EFFECTIVE SYMBOLS

[FIG.6]

EFFECTIVE SYMBOLS

10 [FIG.7]

TRANSMIT SIGNAL

- 101 CONTROL SECTION
- 701 TURBO CODING SECTION

SYSTEMATIC BIT DATA

- 15 PARITY BIT DATA
 - 702 P/S CONVERSION SECTION
 - 703 MODULATION SECTION
 - 102 SPREADING SECTION
 - 103 IFFT SECTION
- 20 104 GI INSERTION SECTION
 - 105 GI INSERTION SECTION
 - 106 GI INSERTION SECTION
 - 107 SELECTION SECTION

RETRANSMISSION INFORMATION

25

INFORMATION INDICATING SYSTEMATIC BIT DATA OR PARITY BIT DATA

[FIG.8]

START

ST801 SYSTEMATIC BIT DATA?

ST802 RETRANSMISSION?

5 ST803 RETRANSMISSION COUNT = 1?

ST804 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/8

OF EFFECTIVE SYMBOL LENGTH

ST805 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/4

OF EFFECTIVE SYMBOL LENGTH

10 ST806 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 3/8

OF EFFECTIVE SYMBOL LENGTH

ST807 OUTPUT

END

15 [FIG.9]

TRANSMIT SIGNAL

DELAY DISTRIBUTION INFORMATION

101 CONTROL SECTION

901 TURBO CODING SECTION

20 SYSTEMATIC BIT DATA

PARITY BIT DATA

902 P/S CONVERSION SECTION

903 MODULATION SECTION

102 SPREADING SECTION

25 103 IFFT SECTION

104 GI INSERTION SECTION

105 GI INSERTION SECTION

106 GI INSERTION SECTION

THRESHOLD VALUE

107 SELECTION SECTION

RETRANSMISSION INFORMATION

DELAY DISTRIBUTION INFORMATION

5

[FIG.10]

RECEIVED SIGNAL

1001 DELAY CIRCUIT

1002 SUBTRACTION CIRCUIT

10 1003 ABSOLUTE VALUE GENERATION CIRCUIT

1004 AVERAGING CIRCUIT

DELAY DISTRIBUTION INFORMATION

[FIG.11]

15 START

ST1101 RETRANSMISSION?

ST1102 RETRANSMISSION COUNT = 1?

ST1103 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/8

OF EFFECTIVE SYMBOL LENGTH

20 ST1104 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/4

OF EFFECTIVE SYMBOL LENGTH

ST1105 DELAY DISTRIBUTION < THRESHOLD VALUE?

ST1106 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 3/8

OF EFFECTIVE SYMBOL LENGTH

25 ST1107 OUTPUT

END

[FIG.12]

- 1201 COUNTER SECTION
- 1202 DELAY SECTION
- 1203 SUBTRACTION SECTION

INFORMATION INDICATING TRANSMISSION TIME INTERVAL

- 5 TRANSMIT SIGNAL
 - 101 CONTROL SECTION
 - 102 SPREADING SECTION
 - 103 IFFT SECTION
 - 104 GI INSERTION SECTION
- 10 105 GI INSERTION SECTION
 - 106 GI INSERTION SECTION
 - 107 SELECTION SECTION

THRESHOLD VALUE

RETRANSMISSION INFORMATION

15

[FIG.13]

START

ST1301 RETRANSMISSION?

ST1302 RETRANSMISSION COUNT = 1?

20 ST1303 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/8

OF EFFECTIVE SYMBOL LENGTH

ST1304 TRANSMISSION TIME INTERVAL ≥ THRESHOLD VALUE?

ST1305 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/4

OF EFFECTIVE SYMBOL LENGTH

25 ST1306 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 3/8

OF EFFECTIVE SYMBOL LENGTH

ST1307 OUTPUT

END

[FIG.14]

TRANSMIT SIGNAL

- 101 CONTROL SECTION
- 5 102 SPREADING SECTION
 - 103 IFFT SECTION
 - 104 GI INSERTION SECTION
 - 105 GI INSERTION SECTION
 - 106 GI INSERTION SECTION
- 10 107 SELECTION SECTION

RETRANSMISSION INFORMATION

INFORMATION INDICATING BAND USAGE SITUATION

[FIG.15]

15 START

ST1501 RETRANSMISSION?

ST1502 RETRANSMISSION COUNT = 1?

ST1503 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/8

OF EFFECTIVE SYMBOL LENGTH

20 ST1504 USED BAND RATIO ≤ THRESHOLD VALUE?

ST1505 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 1/4

OF EFFECTIVE SYMBOL LENGTH

ST1506 SELECT TRANSMIT SIGNAL CONTAINING GI THAT IS 3/8

OF EFFECTIVE SYMBOL LENGTH

25 ST1507 OUTPUT

END

[FIG.16]

TRANSMIT SIGNAL

1601 CONTROL SECTION

1602 SPREADING SECTION

RETRANSMISSION INFORMATION

5 1603 S/P CONVERSION SECTION

1604 P/S CONVERSION SECTION

1605 IFFT SECTION

1606 GI INSERTION SECTION

10 [FIG.17]

START

ST1701 RETRANSMISSION?

ST1702 RETRANSMISSION COUNT = 1?

ST1703 NO REARRANGEMENT

15 ST1704 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 THROUGH

G 4

ST1705 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 AND G3

ST1706 IFFT PROCESSING

END

20

25

[FIG.21]

FREQUENCY

#4m m'th chip of signals 3k+1 through 4k of time T
m'th chip of signals 3k+1 through 4k of time 2T
m'th chip of signals 3k+1 through 4k of time 3T

#3m+1 1st chip of signals 3k+1 through 4k of time T 1st chip of signals 3k+1 through 4k of time 2T

1st chip of signals 3k+1 through 4k of time 3T #3m m'th chip of signals 2k+1 through 3k of time T m'th chip of signals 2k+1 through 3k of time 2T m'th chip of signals 2k+1 through 3k of time 3T

5

10

- 20 #1 1st chip of signals 1 through k of time T

 1st chip of signals 1 through k of time 2T

 1st chip of signals 1 through k of time 3T

 TIME

25 [FIG.22]

FREQUENCY

#4m m'th chip of signals k+1 through 2k of time T m'th chip of signals k+1 through 2k of time 2T

m'th chip of signals k+1 through 2k of time 3T

[FIG.23]

5

20

25

FREQUENCY

#4m m'th chip of signals 1 through k of time T
m'th chip of signals 1 through k of time 2T
m'th chip of signals 1 through k of time 3T

#2m+1 1st chip of signals 1 through k of time T

1st chip of signals 1 through k of time 2T

1st chip of signals 1 through k of time 3T

#2m m'th chip of signals 1 through k of time T

m'th chip of signals 1 through k of time 2T

m'th chip of signals 1 through k of time 3T

#m+1 1st chip of signals 1 through k of time T
 1st chip of signals 1 through k of time 2T
 1st chip of signals 1 through k of time 3T
#m m'th chip of signals 1 through k of time T
 m'th chip of signals 1 through k of time 2T
 m'th chip of signals 1 through k of time 3T

#1 1st chip of signals 1 through k of time T

1st chip of signals 1 through k of time 2T 1st chip of signals 1 through k of time 3T

5 [FIG.24]

TIME

TRANSMIT SIGNAL

1601 CONTROL SECTION

2401 TURBO CODING SECTION

2402 P/S CONVERSION SECTION

10 1602 SPREADING SECTION

RETRANSMISSION INFORMATION

BIT INFORMATION

1603 S/P CONVERSION SECTION

1604 P/S CONVERSION SECTION

15 1605 IFFT SECTION

1606 GI INSERTION SECTION

[FIG.25]

START

20 ST2501 PARITY BIT DATA?

ST2502 RETRANSMISSION?

ST2503 NO REARRANGEMENT

ST2504 RETRANSMISSION COUNT = 1?

ST2505 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 THROUGH

25 G4

ST2506 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 AND G3

ST2507 IFFT PROCESSING

END

[FIG.26]

TRANSMIT SIGNAL

1601 CONTROL SECTION

5 2601 TURBO CODING SECTION

2602 P/S CONVERSION SECTION

1602 SPREADING SECTION

RETRANSMISSION INFORMATION

CHANNEL QUALITY INFORMATION

10 1603 S/P CONVERSION SECTION

1604 P/S CONVERSION SECTION

1605 IFFT SECTION

1606 GI INSERTION SECTION

15 [FIG.27]

START

ST2701 RETRANSMISSION?

ST2702 RETRANSMISSION COUNT = 1?

ST2703 NO REARRANGEMENT

20 ST2704 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 THROUGH

G 4

ST2705 IS CHANNEL QUALITY GOOD?

ST2706 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 AND G3

ST2707 IFFT PROCESSING

25 END

[FIG.28]

2801 COUNTER SECTION

2802 DELAY SECTION

2803 SUBTRACTION SECTION

THRESHOLD VALUE

2804 SIZE COMPARISON SECTION

TRANSMIT SIGNAL

1601 CONTROL SECTION

1602 SPREADING SECTION

1603 S/P CONVERSION SECTION

1604 P/S CONVERSION SECTION

10 1605 IFFT SECTION

1606 GI INSERTION SECTION

RETRANSMISSION INFORMATION

[FIG.29]

15 START

ST2901 RETRANSMISSION?

ST2902 RETRANSMISSION COUNT = 1?

ST2903 NO REARRANGEMENT

ST2904 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 THROUGH

20 G4

ST2905 TRANSMISSION TIME INTERVAL ≥ THRESHOLD VALUE?

ST2906 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 AND G3

ST2907 IFFT PROCESSING

END

25

[FIG.30]

BAND INFORMATION

TRANSMIT SIGNAL

1601 CONTROL SECTION

1602 SPREADING SECTION

1603 S/P CONVERSION SECTION

1604 P/S CONVERSION SECTION

5 1605 IFFT SECTION

1606 GI INSERTION SECTION

RETRANSMISSION INFORMATION

[FIG.31]

10 START

ST3101 RETRANSMISSION?

ST3102 RETRANSMISSION COUNT = 1?

ST3103 NO REARRANGEMENT

ST3104 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 THROUGH

15 G4

ST3105 MARGIN IN BAND?

ST3106 REARRANGEMENT TO ASSIGN SIGNAL \$1 TO G1 AND G3

ST3107 IFFT PROCESSING

END

20

[FIG.32]

TRANSMIT SIGNAL

1601 CONTROL SECTION

3201 TURBO CODING SECTION

25 3202 P/S CONVERSION SECTION

1602 SPREADING SECTION

1603 S/P CONVERSION SECTION

1604 P/S CONVERSION SECTION

1605 IFFT SECTION

1606 GI INSERTION SECTION

SIGNAL INDICATING USED BAND

RETRANSMISSION INFORMATION

5 CHANNEL QUALITY INFORMATION

THRESHOLD VALUE α

THRESHOLD VALUE β

3203 SELECTION SECTION

3204 SIZE COMPARISON SECTION

10 SIGNAL INDICATING RETRANSMISSION ABORT

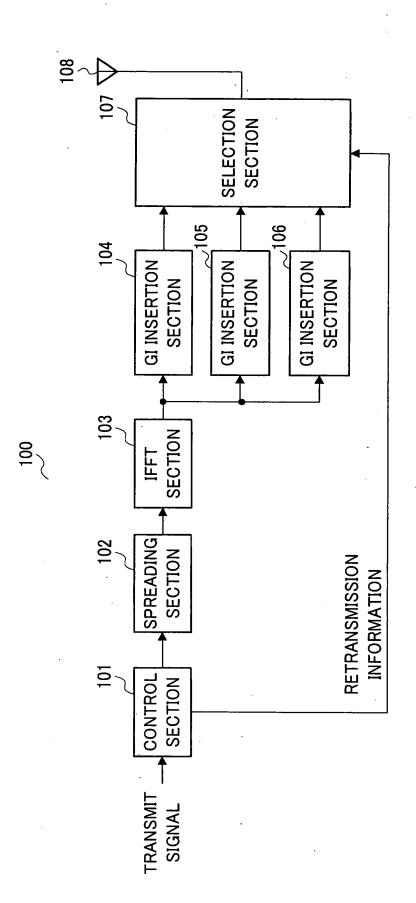


FIG.1

THROUGH 3k OF TIME 3T G2 IST CHIP OF SIGNALS 2k+1 THROUGH 3k OF TIME 3T THROUGH 3k OF TIME 3T	m' TH CHIP OF SIGNALS k+1 THROUGH 2k OF TIME 3T 1ST CHIP OF SIGNALS k+1 THROUGH 2k OF TIME 3T THROUGH 2k OF TIME 3T	m' TH CHIP OF SIGNALS 1 THROUGH k OF TIME 3T 1ST CHIP OF SIGNALS 1 THROUGH k OF TIME 3T	TIME
	THROUGH 2k OF TIME 2T 1ST CHIP OF SIGNALS k+1 THROUGH 2k OF TIME 2T	m' TH CHIP OF SIGNALS 1 THROUGH & OF TIME 2T 1ST CHIP OF SIGNALS 1 THROUGH & OF TIME 2T	
m' TH CHIP OF SIGNALS k+1	THROUGH 2k OF TIME T 1ST CHIP OF SIGNALS k+1 THROUGH 2k OF TIME T	m' TH CHIP OF SIGNALS 1 THROUGH k OF TIME T 1ST CHIP OF SIGNALS 1 THROUGH k OF TIME T	
#5m_	#	£ # .	

FIG.2

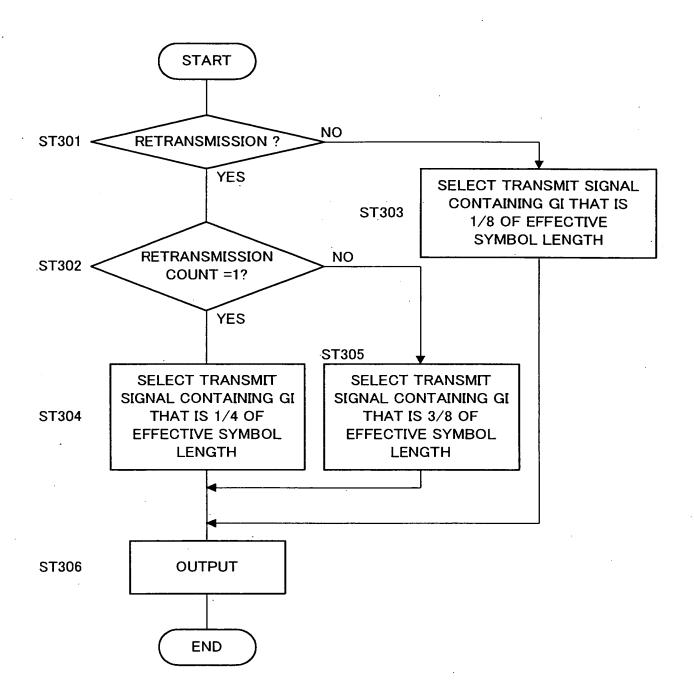


FIG.3

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FIG.4

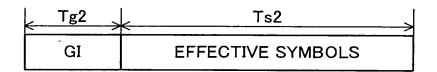


FIG.5

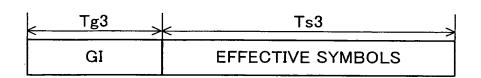


FIG.6

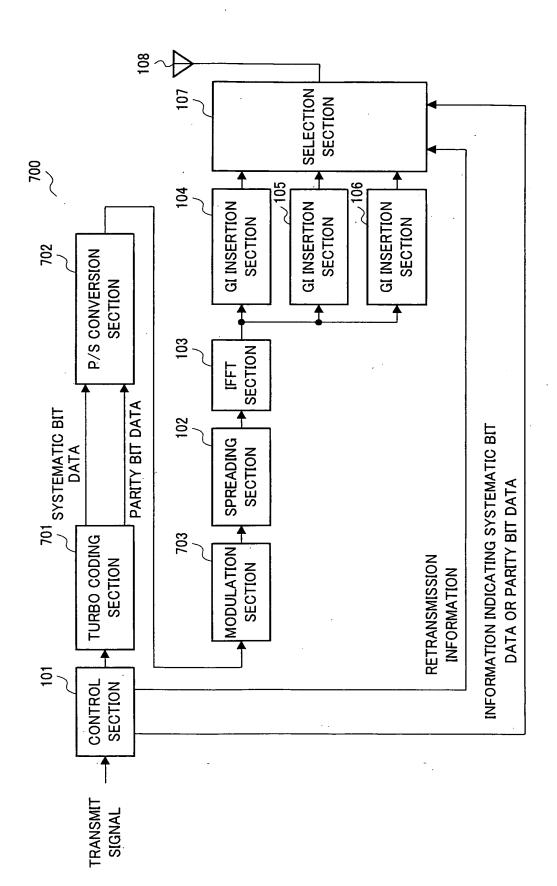


FIG.7

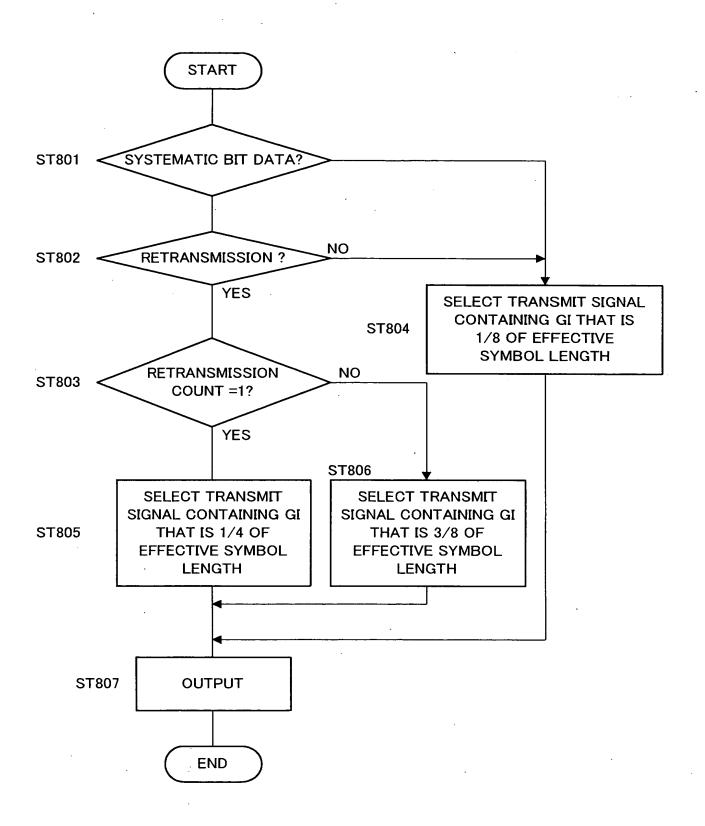


FIG.8

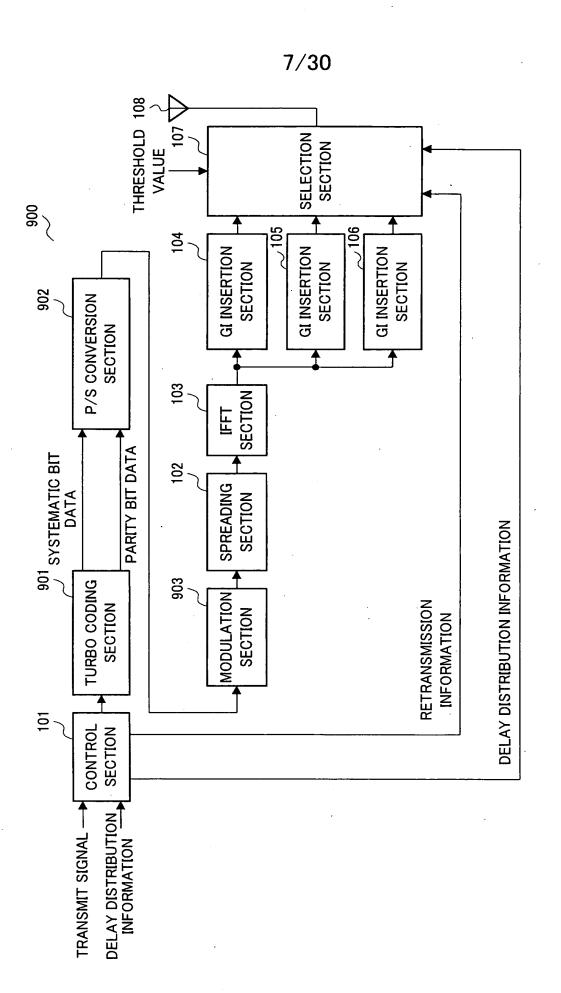


FIG.9

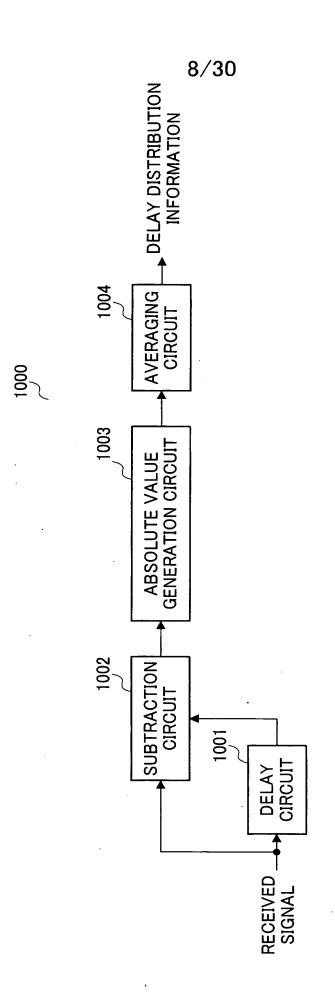


FIG. 10

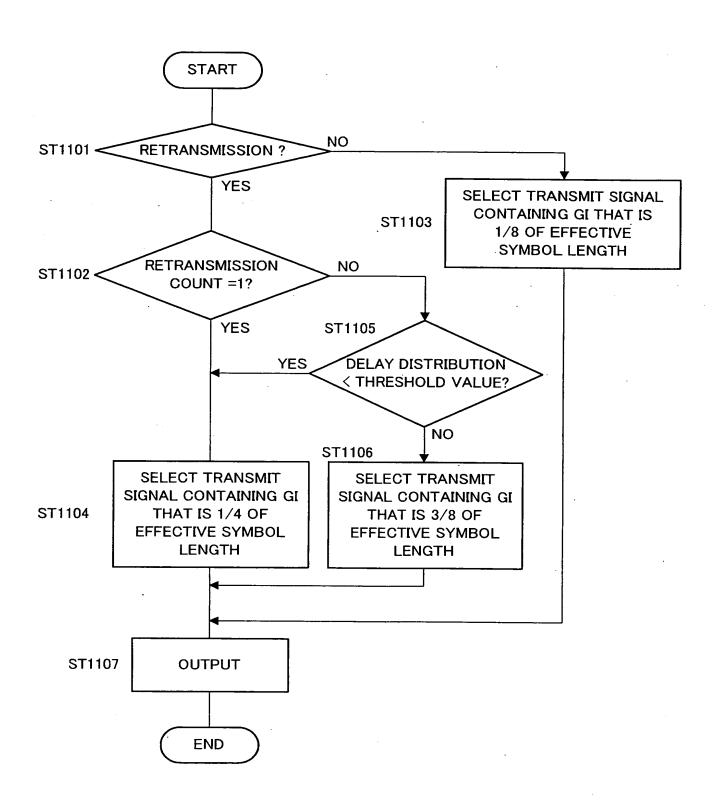


FIG.11

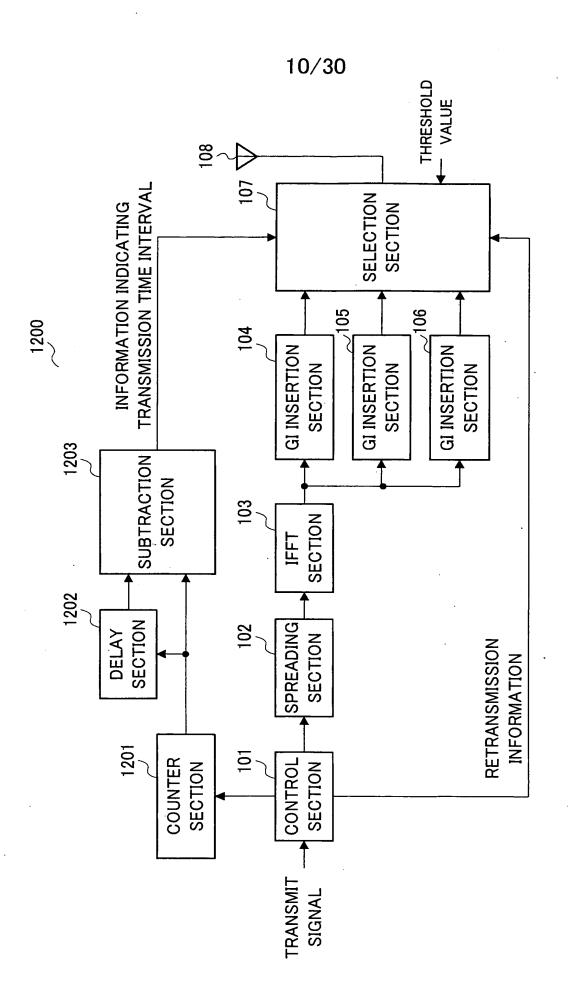


FIG.12

The contraction of

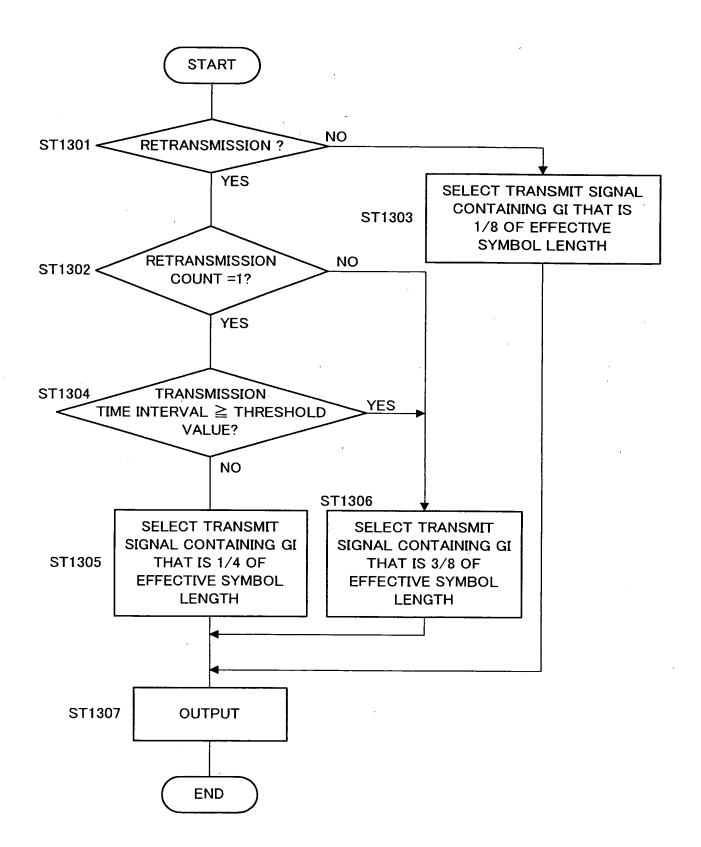


FIG.13

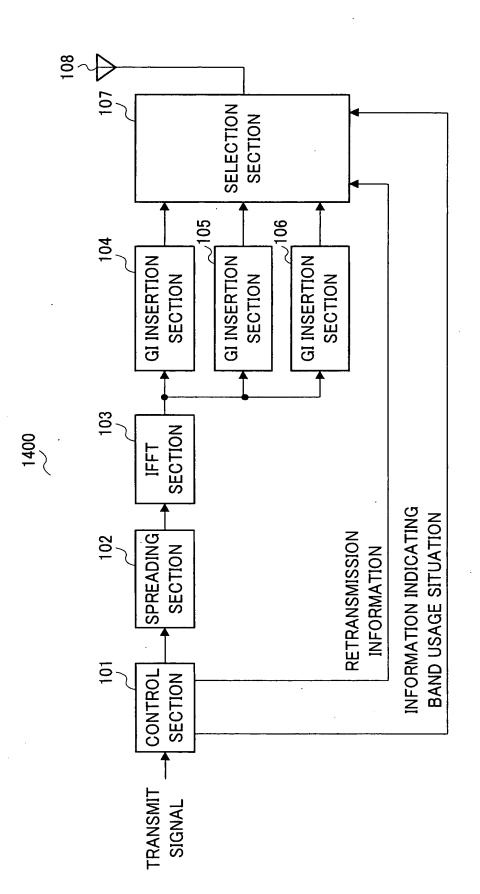


FIG.14

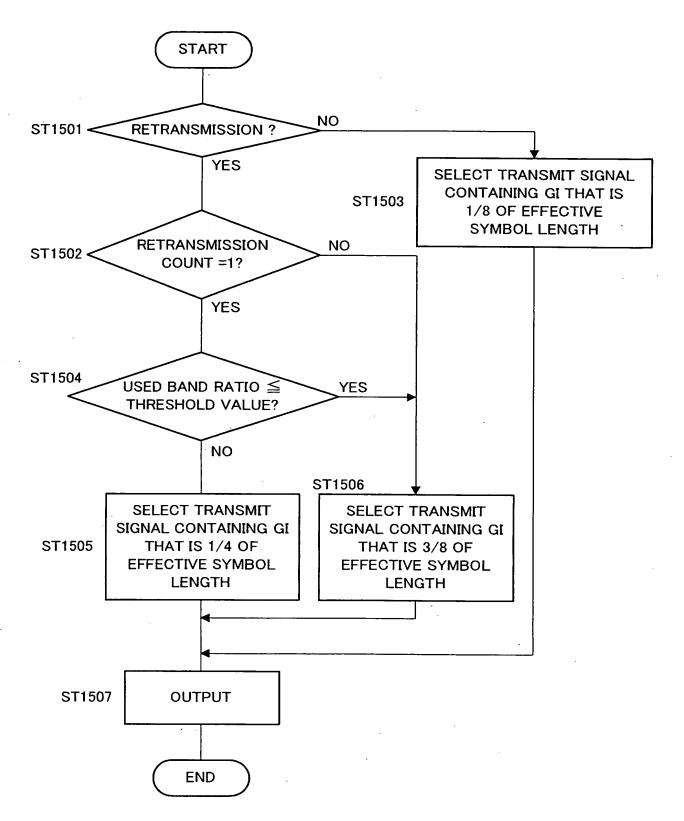


FIG.15

CAMPINE OF

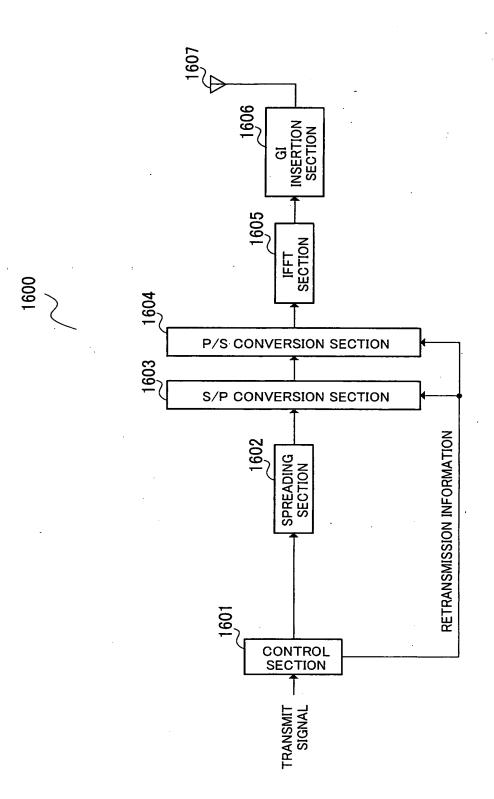


FIG. 16

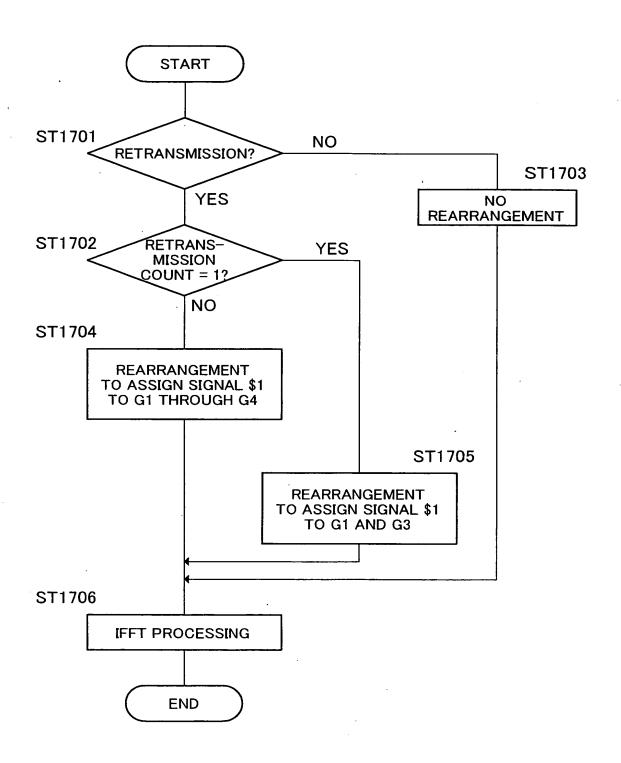


FIG.17

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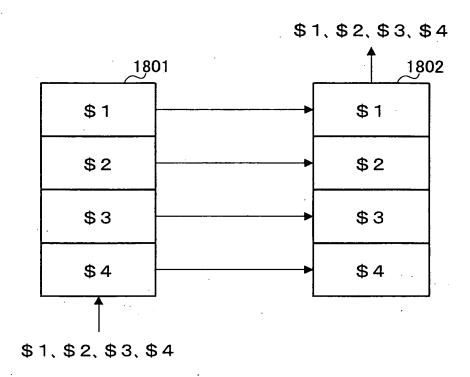


FIG.18

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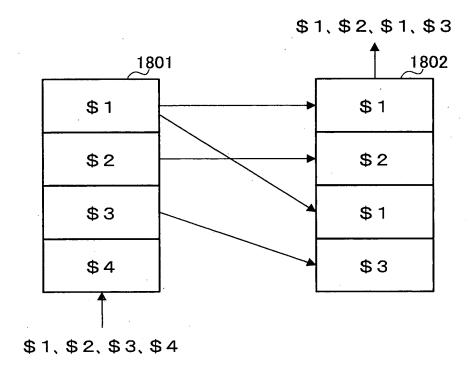


FIG.19

The state of the s

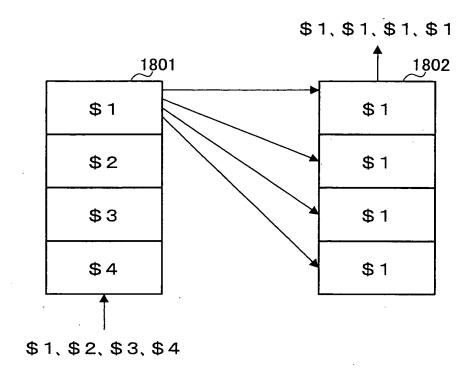


FIG.20

FIG.21

<u> </u>	;		\ 			£5 			→ 	
:	:	:		:	:		:	:		:
m'th chip of signals k+1 through 2k of time 3T	1st chip of signals k+1 through 2k of time 3T	m'th chip of signals 1 through k of time 3T		1st chip of signals 1 through k of time 3T	m'th chip of signals k+1 through 2k of time 3T		1st chip of signals k+1 through 2k of time 3T	m'th chip of signals 1 through k of time 3T		1st chip of signals 1 through k of time 3T
m'th chip of signals k+1 through 2k of time 2T	ist chip of signals k+1 through 2k of time 2T	m'th chip of signals 1 through k of time 2T		1st chip of signals 1 through k of time 2T	m'th chip of signals k+1 through 2k of time 2T		1st chip of signals k+1 through 2k of time 2T	m'th chip of signals 1 through k of time 2T		1st chip of signals 1 through k of time 2T
m'th chip of signals k+1 through 2k of time T	1st chip of signals k+1 through 2k of time T	m'th chip of signals 1 through k of time T		1st chip of signals 1 through k of time T	m'th chip of signals k+1 through 2k of time T		1st chip of signals k+1 through 2k of time T	m'th chip of signals 1 through k of time T		1st chip of signals 1 through k of time T
#4m	#3m+1	#3 ^m		#2m+1	#2m		#m+1	##		#

FIG.22

<u> </u>			\ 			~ _{			_ }_ G4	
:	:	:		:	:		:	:		:
m 'th chip of signals 1 through k of time 3T	1st chip of signals 1 through k of time 3T	m 'th chip of signals 1 through k of time 3T		1st chip of signals 1 through k of time 3T	m 'th chip of signals 1 through k of time 3T		1st chip of signals 1 through k of time 3T	m 'th chip of signals 1 through k of time 3T		1st chip of signals 1 through k of time 3T
m 'th chip of signals 1 through k of time 2T	1st chip of signals 1 through k of time 2T	m¹th chip of signals 1 through k of time 2T	•••	1st chip of signals 1 through k of time 2T	m ' th chip of signals 1 through k of time 2T		1st chip of signals 1 through k of time 2T	m¹th chip of signals 1 through k of time 2T		1st chip of signals 1 through k of time 2T
m'th chip of signals 1 through k of time T	1st chip of signals 1 through k of time T	m ' th chip of signals 1 through k of time T		1st chip of signals 1 through k of time T	m 'th chip of signals 1 through k of time T	-	1st chip of signals 1 through k of time T	m¹th chip of signals 1 through k of time T		1st chip of signals 1 through k of time T
#4m	#3m+1	#3m		#2m+1	#2m		#m+1	, w#		#

FIG.23

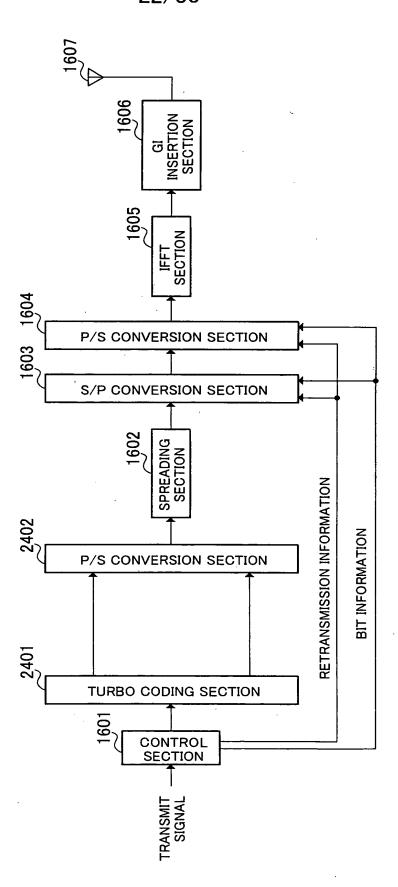
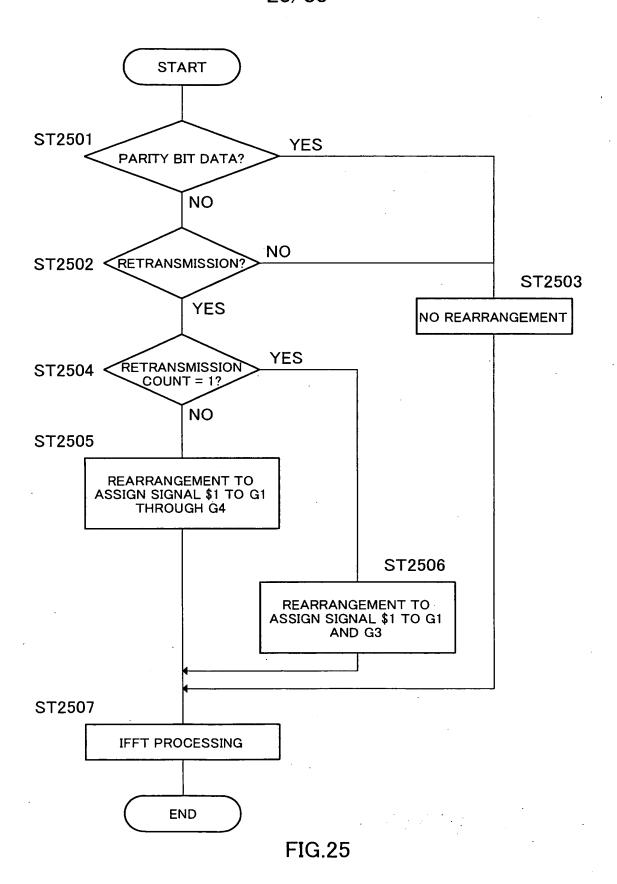


FIG.24





2600

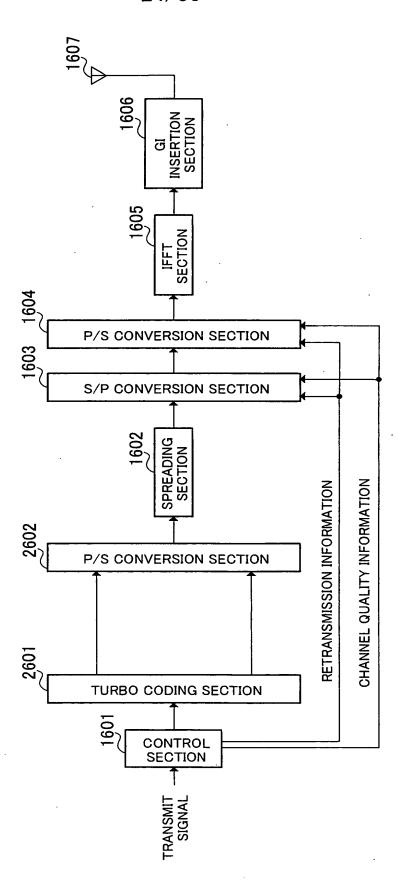


FIG.26

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P. F. Santager

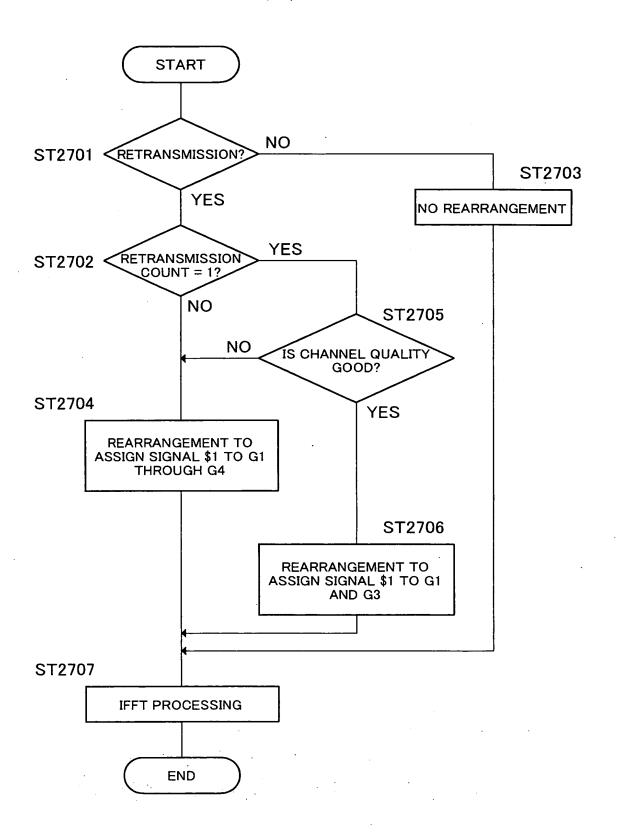


FIG.27

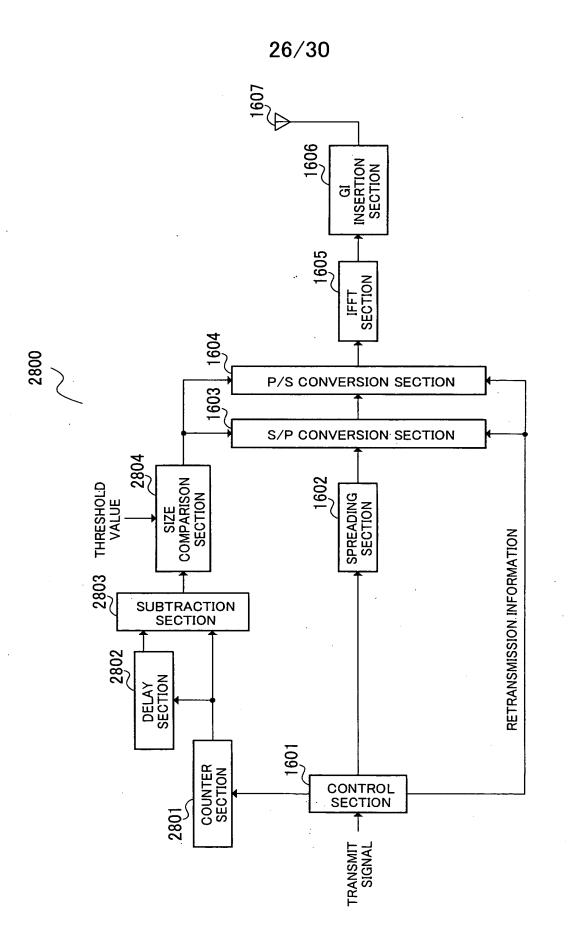


FIG.28

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The following the factors

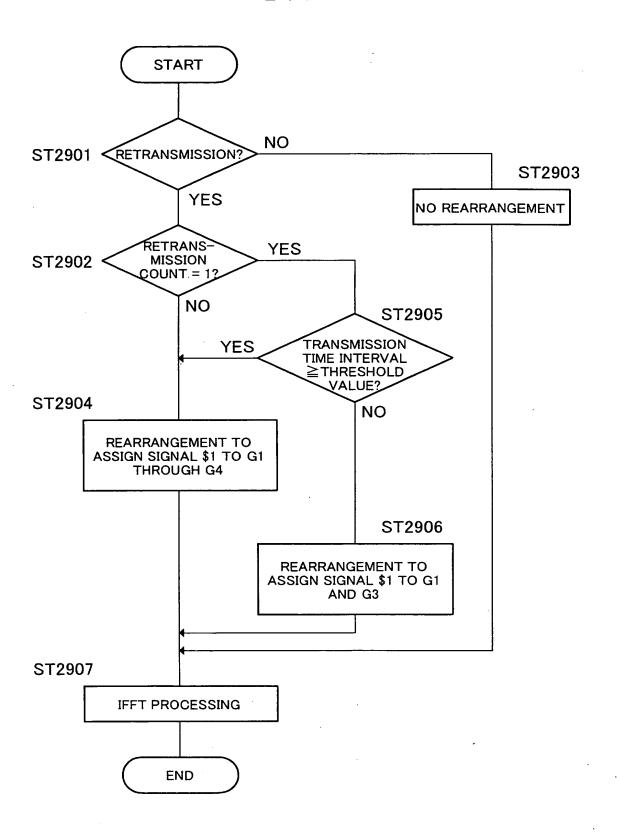
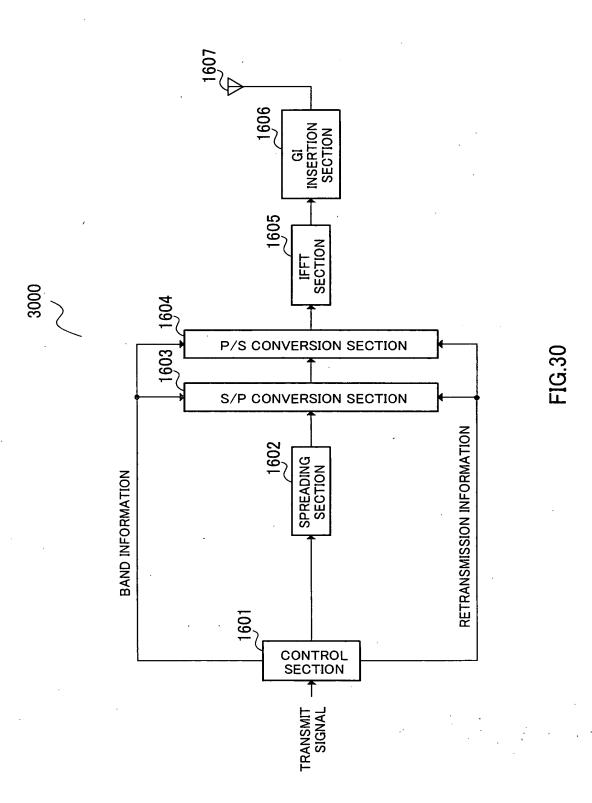


FIG.29



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A Part of the State of the Stat

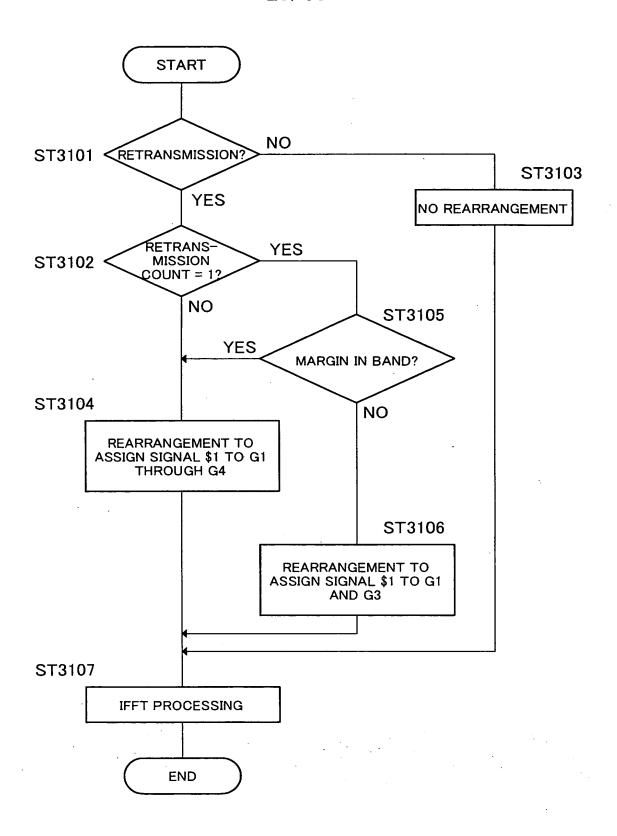


FIG.31

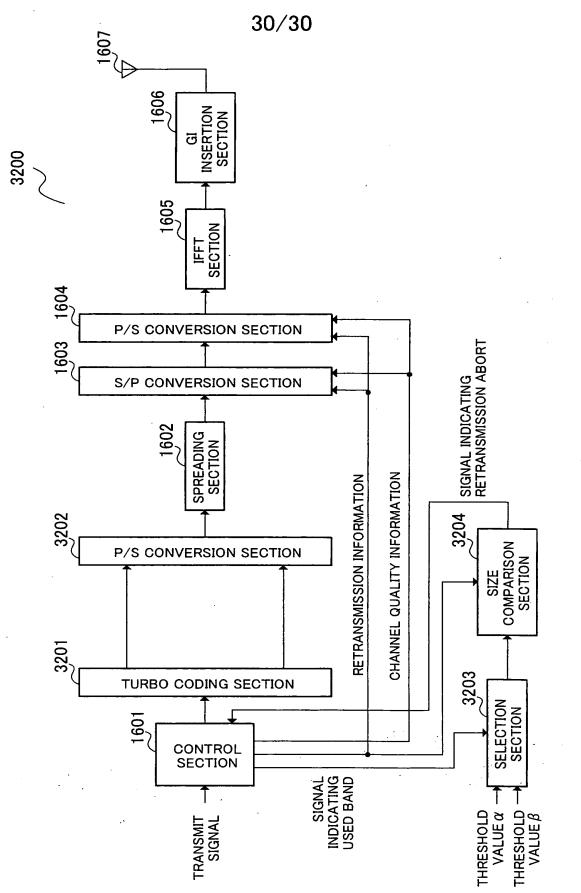


FIG.32